

# ServerWorks ServerSet™ III LE

## Product Overview

Features	Benefits
133 MHz Front Side Bus Capability	<ul style="list-style-type: none"> <li>• Supports both 100 MHz and 133 MHz system bus for single and dual processor configurations</li> <li>• Significantly increases the bandwidth available for multiprocessor servers</li> </ul>
PC133 SDRAM Technology	<ul style="list-style-type: none"> <li>• Superior memory performance in terms of higher bandwidth and lower access latency</li> <li>• Increases flexibility of system design for memory configurations</li> <li>• Proven technology with low cost structure</li> </ul>
Server Optimized Memory Technology	<ul style="list-style-type: none"> <li>• Advanced ECC algorithms that maintain system data integrity</li> <li>• Supports a large capacity of memory up to 4-GBytes</li> <li>• Supports wide range of memory DIMMs including 64, 128, 256, 512 MByte, and 1-GByte</li> </ul>
Advanced 64-bit PCI Technology	<ul style="list-style-type: none"> <li>• Maximizes IO bandwidth for the next generation of 64-bit PCI cards</li> <li>• Supports up to 2 full 64-bit/66 MHz PCI busses</li> <li>• Supports up to 5 64-bit/33 MHz PCI busses</li> </ul>
Advanced IO Caching Technology	<ul style="list-style-type: none"> <li>• Increases sustainable PCI bus bandwidth by off loading system resources</li> <li>• Reduces Front Side bus snoop traffic for enhanced multiprocessing performance</li> </ul>
Legacy 32-bit PCI Technology	<ul style="list-style-type: none"> <li>• Dedicated 32-bit PCI bus for legacy IO and south bridge interconnect</li> </ul>
Scalable Architecture	<ul style="list-style-type: none"> <li>• Increases flexibility of system design for processor, memory, and PCI configurations</li> </ul>
Full Peer-to-Peer Support	<ul style="list-style-type: none"> <li>• Allows full peer-to-peer transactions between PCI busses</li> <li>• Increases options for Intelligent IO and Server Management cards</li> </ul>
Integrated IOAPIC	<ul style="list-style-type: none"> <li>• Supports full 32-APIC entries</li> <li>• Removes the need for a separate IOAPIC chip</li> <li>• BOM cost savings, additional board real estate, and increased design flexibility</li> </ul>
Server Management Bus Technology	<ul style="list-style-type: none"> <li>• Supports the industry standard Server Management Bus (SMBus)</li> </ul>
ATA66 (IDE Drives)	<ul style="list-style-type: none"> <li>• Enhances HDD access for faster reads and writes</li> </ul>
4 USB Ports	<ul style="list-style-type: none"> <li>• Provides the end user with more options to connect peripherals</li> </ul>

<b>ServerSet™ III LE Components:</b>	<b>Package:</b>
NB6635 North Bridge 3.0LE	<ul style="list-style-type: none"> <li>• 576 Tape Ball Grid Array (TBGA)</li> </ul>
IB6566 South Bridge	<ul style="list-style-type: none"> <li>• 352 Plastic Ball Grid Array (PBGA)</li> </ul>

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